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	Application No.	Applicant(s)
	09/943,888	MANETAKIS, KONSTANTINOS
Notice of Allowability	Examiner	Art Unit
	Linh V. Nguyen	2819
The MAILING DATE of this communication appearable claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) of NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGOT of the Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in other appropriate comm GHTS. This application is	n this application. If not included unication will be mailed in due course. THIS
1. This communication is responsive to <u>9/6/05</u> .		
2. ⊠ The allowed claim(s) is/are <u>1-17,19-30</u> .		
 3. ☐ Acknowledgment is made of a claim for foreign priority und a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 		or (f).
2. Certified copies of the priority documents have	been received in Application	on No
Copies of the certified copies of the priority doc	uments have been receive	d in this national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		•
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONMETHIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submit	ENT of this application.	
INFORMAL PATENT APPLICATION (PTO-152) which give:		
5. X CORRECTED DRAWINGS (as "replacement sheets") must	be submitted.	•
(a) ☐ including changes required by the Notice of Draftsperso	on's Patent Drawing Revie	w (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date		
(b) including changes required by the attached Examiner's Paper No./Mail Date 9/24/05.	Amendment / Comment o	r in the Office action of
Identifying indicia such as the application number (see 37 CFR 1.1 each sheet. Replacement sheet(s) should be labeled as such in the		
 DEPOSIT OF and/or INFORMATION about the depos attached Examiner's comment regarding REQUIREMENT F 		
Attachment(s)	E Matica of Ir	oformal Datast Application (DTO 152)
1. Notice of References Cited (PTO-892)		nformal Patent Application (PTO-152)
 Notice of Draftperson's Patent Drawing Review (PTO-948) MInformation Disclosure Statements (PTO-1449 or PTO/SB/08) 	Paper No.	 Interview Summary (PTO-413), Paper No./Mail Date <u>9/24/05</u>. Examiner's Amendment/Comment
Paper No./Mail Date <u>1/28/03,10/20/03</u>		Statement of Reasons for Allowance
 Examiner's Comment Regarding Requirement for Deposit of Biological Material 		
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DETAILED ACTION

1. This office action is in response to RCE filed on 9/6/05. Claims 1, 3, 4, 10, 17, and 19 have been amended. Claims 23 – 30 have been added. Claims 1 – 30 are pending on this application.

Drawings

2. The drawings filed on 8/30/01 are acceptable subject to correction of the informalities of Fig. 1 - 4. In order to avoid abandonment of this application, the formal drawing is required in reply to the Office action. The correction will not be held in abeyance.

EXAMINER'S AMENDMENT

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Donald C. Kordieh on 9/23/05.

The application has been amended as follows:

Claim 10, on line 13 of the claim, after "control currents" add the following:

- - ; wherein the plurality of MOS transistor devices comprise first, second, third and fourth MOS transistor devices, wherein the first MOS transistor device is coupled

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the first control current, the second MOS transistor device is coupled to a current that equals in the sum of the first and second control currents, the third MOS transistor device is coupled to the second control current, and a fourth MOS transistor device is coupled to a bias control current - -

Claim 19, on line 1 of the claim, change "18" to - - 10 - - Cancel claim 18.

Allowable Subject Matter

4. Claims 1 - 17, and 19 - 30 are allowed.

The following is an examiner's statement of reasons for allowance:

With respect to claims 1 and 10, in addition to other elements of translinear loop in each respective claim, the prior art considered individual or in combination, fails to fairly show or suggest a class AB output stage circuit comprising: first, second, third and fourth MOS transistor device configures in a translinear loop; and wherein the first MOS transistor device is coupled the first control current, the second MOS transistor device is coupled to a current that equals in the sum of the first and second control currents, the third MOS transistor device is coupled to the second control current, and a fourth MOS transistor device is coupled to a bias control current.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Prior Art

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Contact Information

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh Van Nguyen whose telephone number is (571) 272-1810. The examiner can normally be reached from 8:30 – 5:00 Monday-Friday. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Robert Pascal can be reached at (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (571-273-8300) for regular communications and (571-273-8300) for After Final communications.

Lullarfyer

9/24/05

Linh Van Nguyen

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